generated across the third and fourth magnetoresistors and so that the first and second fields point in substantially opposite directions thereby producing an output across the first and second isolator output terminals commensurate with the input current.

Please add the following claims:

32. The integrated signal isolator of claim 1 wherein the input strap has a first portion running along a length of the two of the magnetoresistors and a second portion running along a length of the other two of the magnetoresistors.

33. The integrated signal isolator of claim 32 wherein the first portion runs along the length of the first and second magnetoresistors and the second portion runs along the length of the third and fourth magnetoresistors.

34. The integrated signal isolator of claim 1 further comprising a set/reset strap positioned to generate a set/reset magnetic field over the magnetoresistors.

35. The integrated signal isolator of claim 34 wherein the set/reset strap perpendicularly crosses a